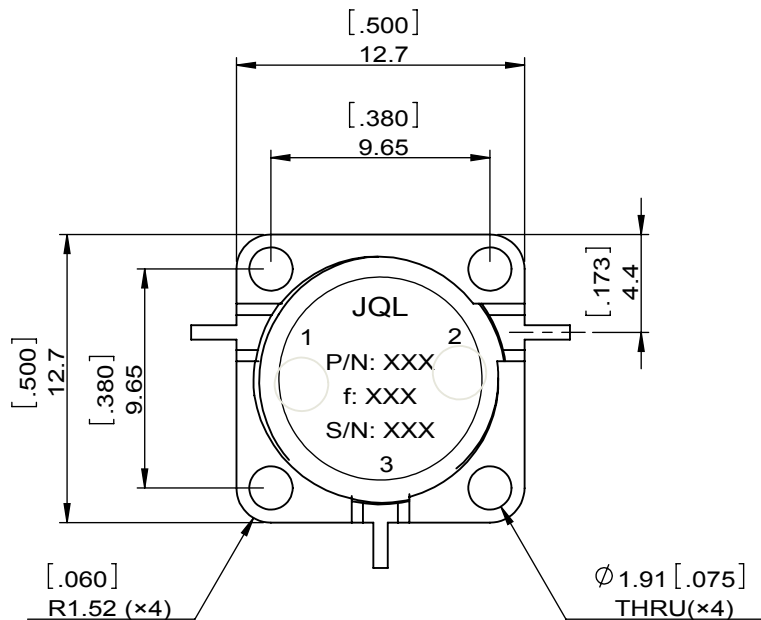
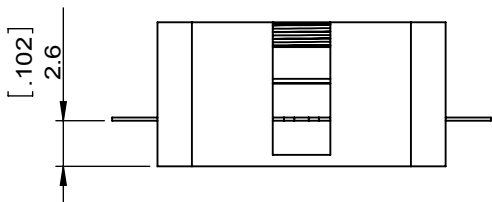
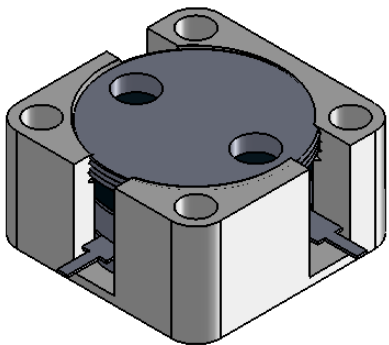


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Document No.	QR4-700-T-003	REV.	A/0	REV.	DESCRIPTION	REVISOR	DATE	APPROVED			
		0				JP.Yang	07.18.2013	David			



SPECIFICATIONS	
Part's Number	JCD3500T3650VM2
Frequency(GHz)	3.500~3.650
Direction	CW
Insertion Loss(dB)MAX	0.30
Isolation(dB)MIN	23.0
Return Loss(dB)MIN	21.0
3rd IMD(dBc)MAX	-
Power FWD/REV/PK(Watt)	50/50/-
Operating Temperature(°C)	-40~+85
Storage Temperature(°C)	-55~+125
2nd/3rd Harmonic(dBc)	-
Termination/Attenuator(Watt/dB)	-
Tab(W×LG×THK)(mm[Inch])	0.7[.028]×2.0[.078]×0.2[.008]
Additional Notes	-

- NOTES:
- STANDARD INPUT/OUTPUT 50OHM IMPEDANCE
 - BEO FREE,LEAD FREE,NO Nd MATERIAL
 - RoHS COMPLIANT
 - S-PARAMETERS TO BE MEASURED BYCONNECTING PORT 1 AND PORT 2 TO VNA,PORT 3 TO LOAD
 - LOAD RETURN LOSS IS 30dB OR HIGHER WHEN TUNING



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JQL JQL Electronic Inc		500 Lake Cook Road, Suite 350 Deerfield, IL 60015, USA		Tel: 1(888)236-9828 Fax: 1(888)823-2902	
MATERIAL: STEEL	DWG NO: JCD3500T3650VM2				
SURFACE TREATMENT: NICKEL PLATING	DWG BY:	JP.Yang	07.18.2013	SACLE	3:1
DESCRIPTION: DROP-IN CIRCULATOR	CHK'D BY:				
	APPV'D BY:				A4
	Unit: mm[inch]	Tolerance: Liner: ±0.25 Anguar: ±0.5°	The First:		