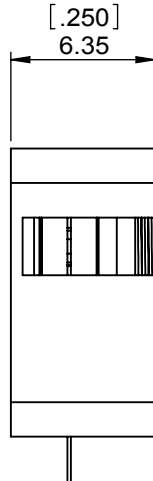
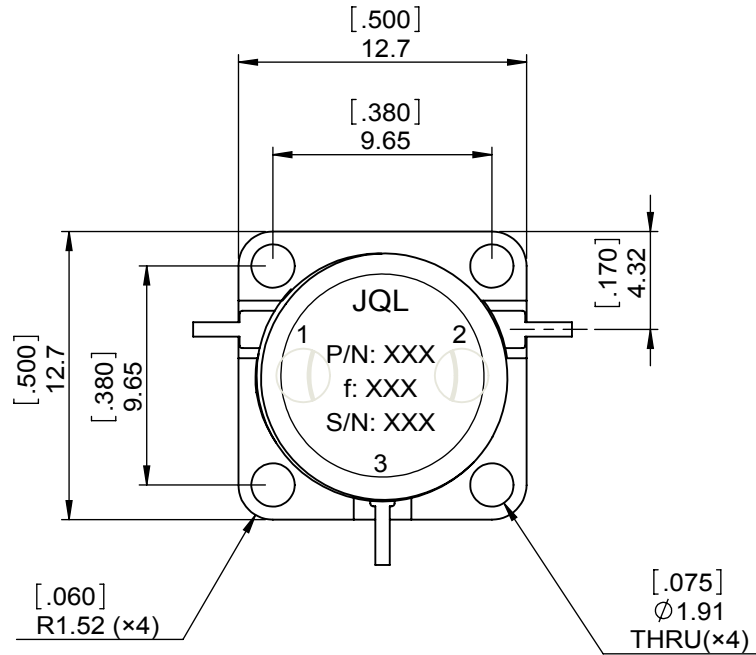
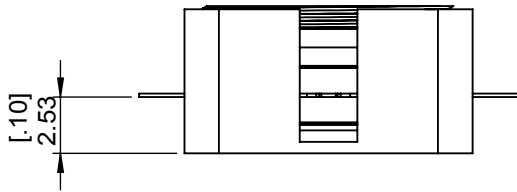
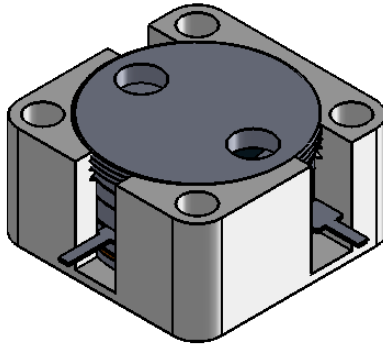


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Document No.	QR4-700-T-003					REV.	DESCRIPTION	REVISOR	DATE	APPROVED	
REV.	A/0					0		M.Hao	10.25.2013	David	



SPECIFICATIONS	
Part's Number	JCD9050T9450VM0
Frequency(GHz)	9.050~9.450
Direction	CW
Insertion Loss(dB)MAX	0.40
Isolation(dB)MIN	20.0
Return Loss(dB)MIN	20.0
3rd IMD(dBc)MAX	-
Power FWD/REV/PK(Watt)	5/5/-
Operating Temperature(°C)	-30~+75
Storage Temperature(°C)	-
2nd/3rd Harmonic(dBc)	-
Termination/Attenuator(Watt/dB)	-
Tab(W×LG×THK)(mm[Inch])	.635[.03]×2.0[.078]×.13[.005]
Additional Notes	-

- NOTES:
- STANDARD INPUT/OUTPUT 50OHM IMPEDANCE
  - BEO FREE,LEAD FREE,NO Nd MATERIAL
  - RoHS COMPLIANT
  - S-PARAMETERS TO BE MEASURED BYCONNECTING PORT 1 AND PORT 2 TO VNA,PORT 3 TO LOAD
  - LOAD RETURN LOSS IS 30dB OR HIGHER WHEN TUNING



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<b>MATERIAL:</b> STEEL		<b>DWG NO:</b> JCD9050T9450VM0			
<b>SURFACE TREATMENT:</b> NICKEL PLATING		DWG BY:	M.Hao	10.25.2013	SACLE
<b>DESCRIPTION:</b> DROP-IN CIRCULATOR		CHK'D BY:			
		APPV'D BY:			3:1
		Unit: mm[inch]	Tolerance: Liner: ±0.25 Anguar: ±0.5°	The First:	A4